

Monitoring Hyperproperties

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Hyperproperties

Clarkson&Schneider '10:

Hyperproperty H : a set of sets of traces

System K satisfies H iff $Traces(K) \in H$.

Many information-flow properties can be formalized as hyperproperties.

Noninterference as hyperproperty:

$$\{T \subseteq 2^{Traces} \mid \forall t, t' \in T : t =_{I_{public}} t' \Rightarrow t =_{O_{public}} t'\}$$

Trace equality, observational determinism, noninference, declassification, ...

A simple information-flow property

“All executions have the light on at the same time.”



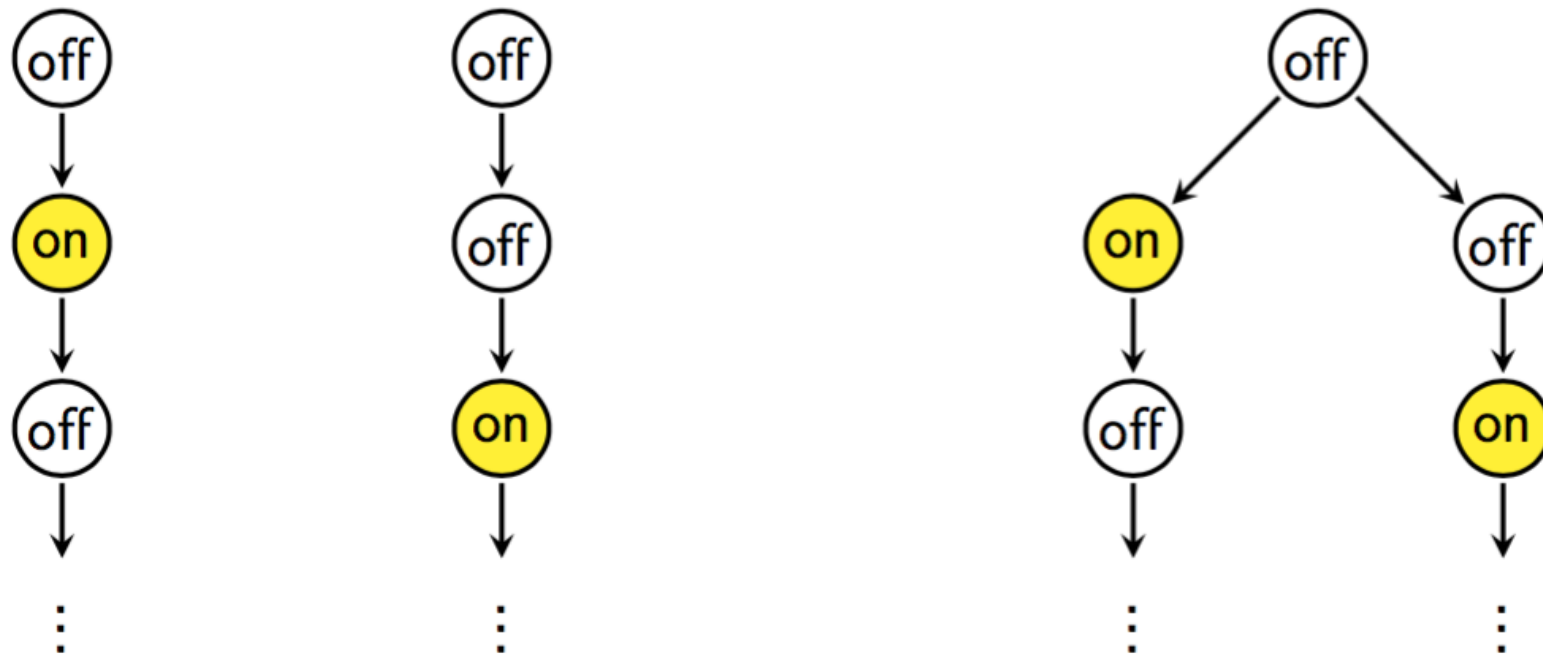
“For all pairs of traces and all points in time, the light is “on” on the one trace iff it is “on” on the other trace.”

LTL ?

Syntax: $\varphi ::= a_\pi \mid X\psi \mid G\psi \mid F\psi \mid \psi U \psi$

Semantics: $K \models \varphi$ iff $Traces(K) \subseteq Traces(\varphi)$

“All executions have the light on at the same time.”

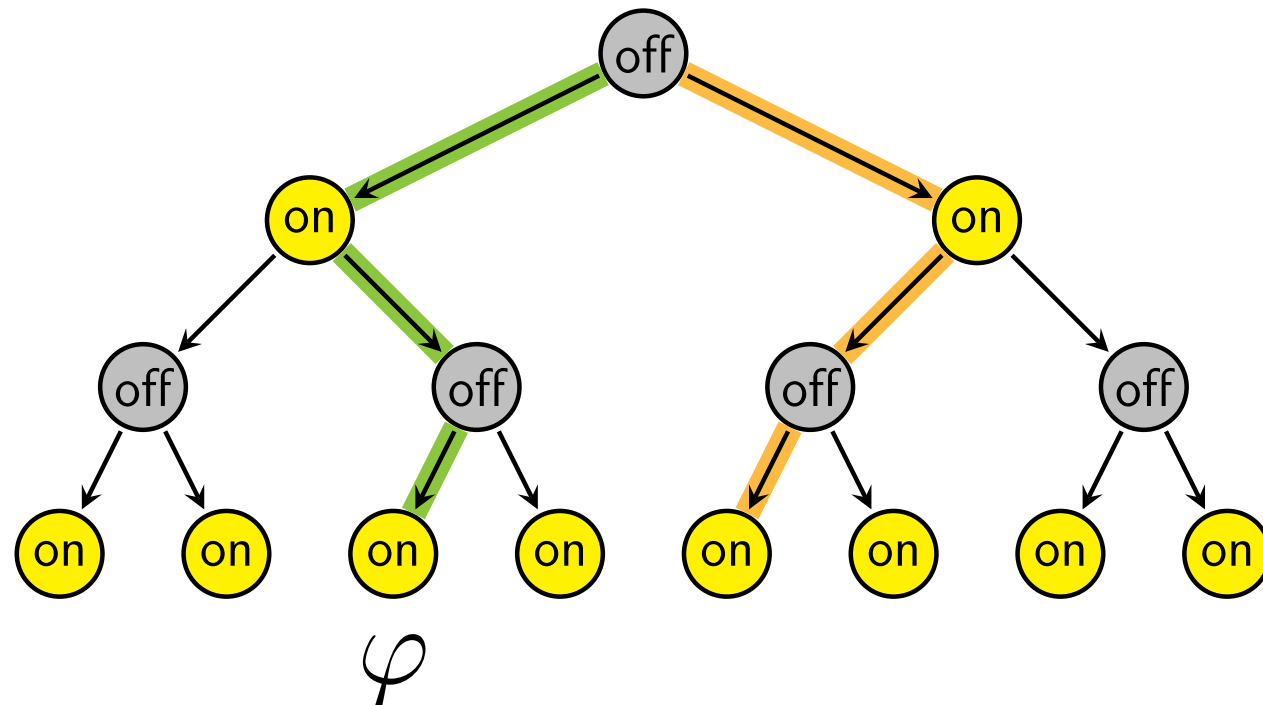


CTL* ?

Syntax: $\varphi ::= a \mid A\varphi \mid E\varphi \mid X\varphi \mid G\varphi \mid \varphi U \varphi \mid \dots$

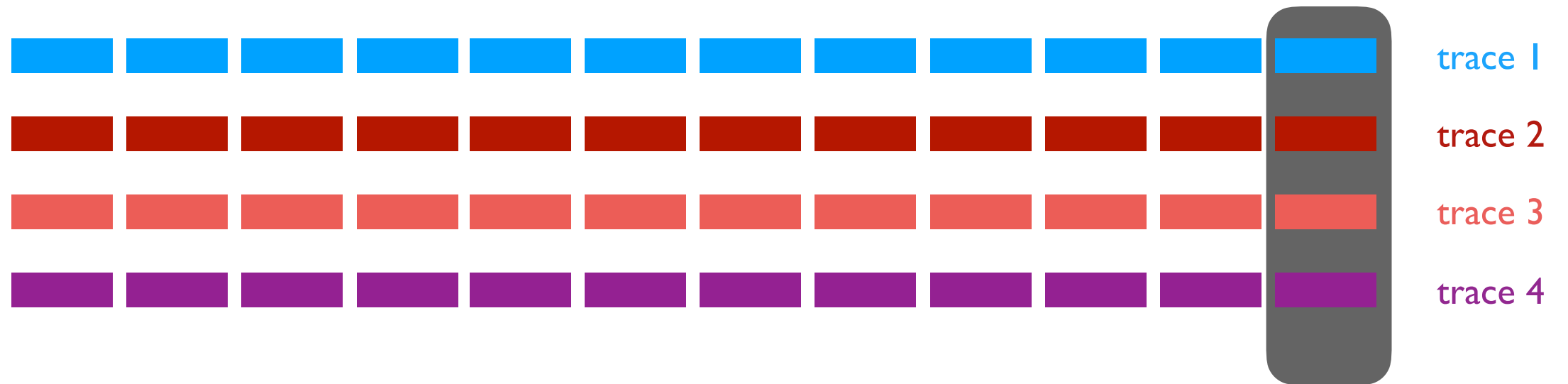
Semantics: $K \models A\varphi$ iff for all $p \in \text{Paths}(K) : p \models \varphi$

“All executions have the light on at the same time.” $AA\varphi$?

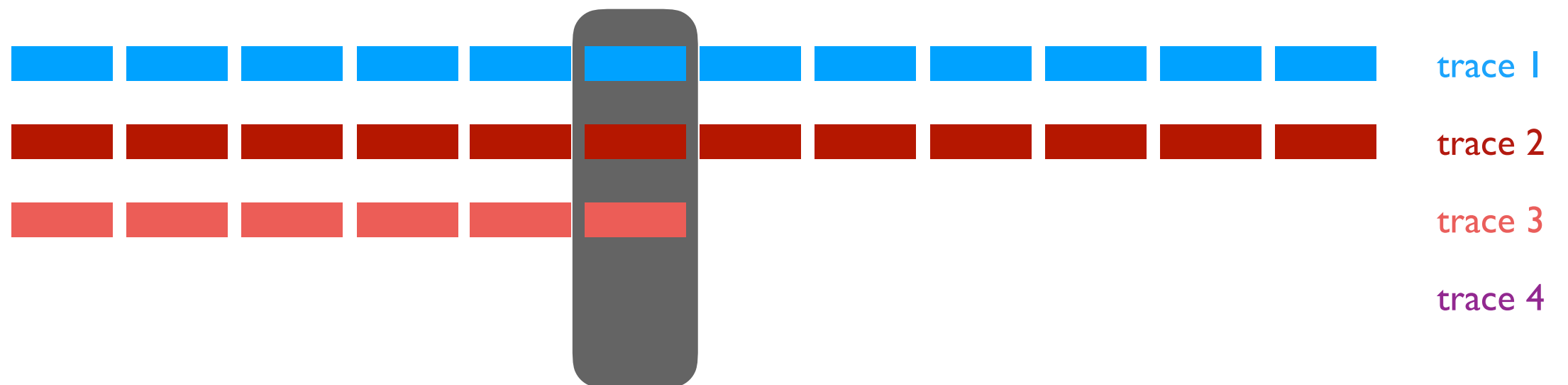


Monitoring

- Parallel monitoring

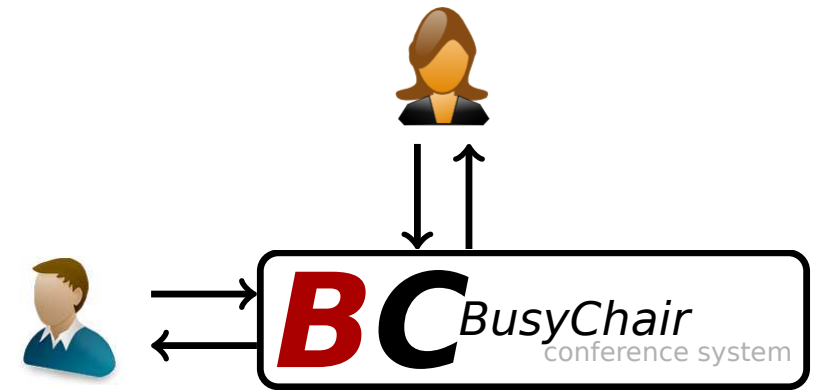


- Sequential monitoring



Parallel Monitoring

Example: conference management system with  **author** and  **pc** traces



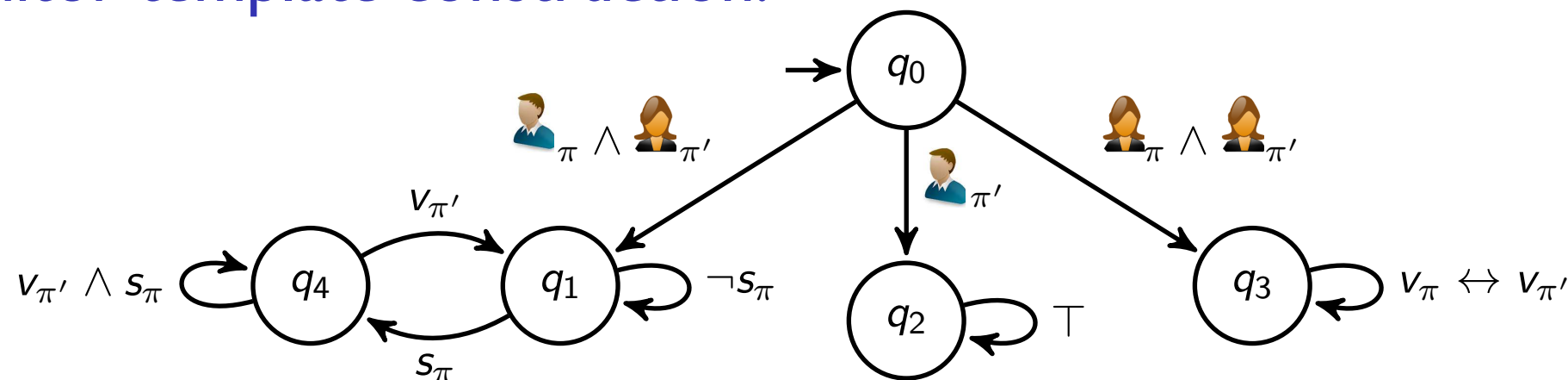
„No paper submission is lost“:

- every submission (s) is visible (v) to every PC member
- when comparing two  pc traces, they must agree on v

$$\forall \pi. \forall \pi'. (\text{author } \pi \wedge \text{pc } \pi') \rightarrow X G (s_\pi \rightarrow X v_{\pi'}) \wedge$$

$$(\text{pc } \pi \wedge \text{pc } \pi') \rightarrow X G (v_\pi \leftrightarrow v_{\pi'})$$

Monitor template construction:

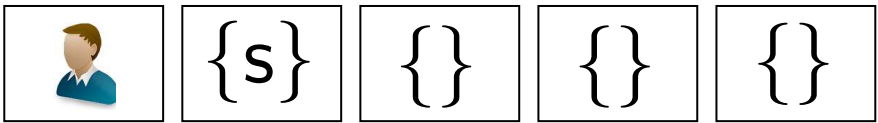


The monitor runs in parallel over all permutations of the traces.

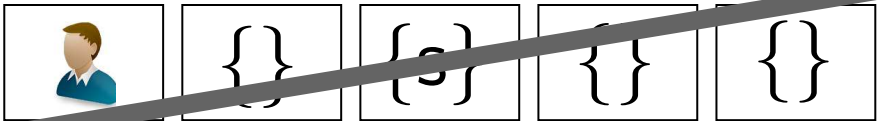
Sequential Monitoring

- Naive approach:
 - Store growing set of traces
 - Repeatedly apply parallel monitoring
- Memory explosion!
 - Storing every trace leads to unbounded memory consumption
- Key: effective memory management
 - trace analysis: discard traces that are dominated by other traces
 - specification analysis: exploit symmetry, transitivity, reflexivity
 - trace set representation: organize traces by prefix

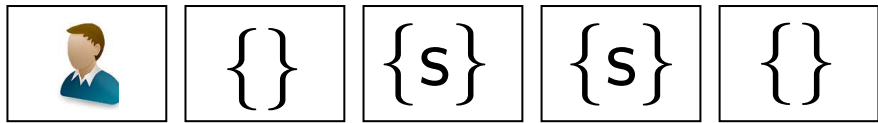
Trace Analysis: Example



An author  submits a paper

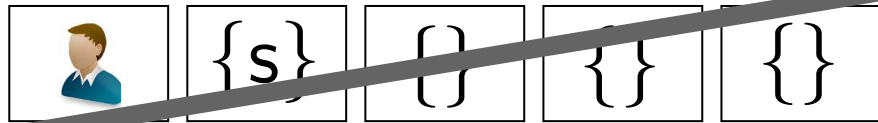


Another author  submits a paper

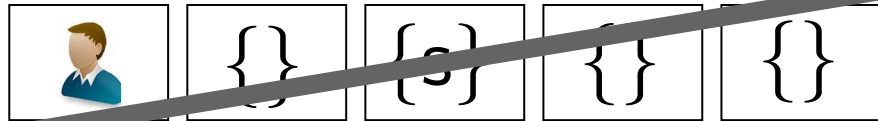


An author  submits two papers

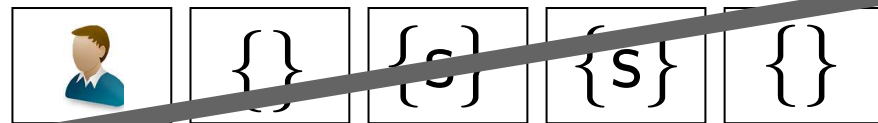
Trace Analysis: Example



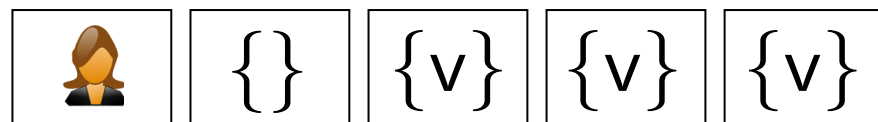
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Another author  submits a paper

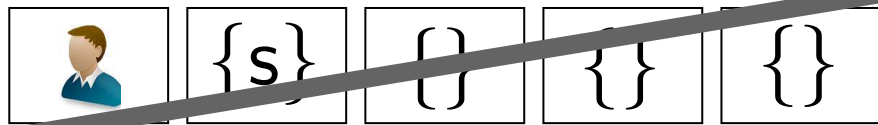


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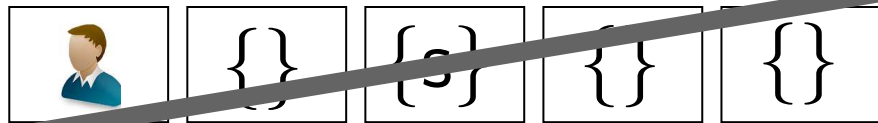


A pc member  observes three submissions

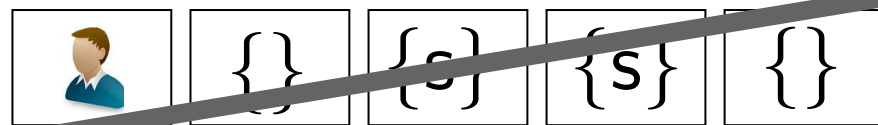
Trace Analysis: Example



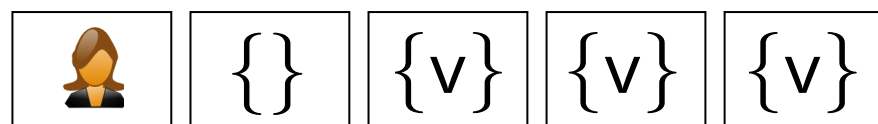
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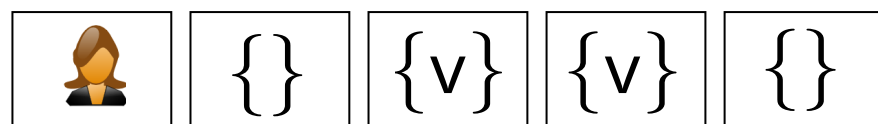
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An author  submits two papers



A pc member  observes three submissions



A pc member  observes two submissions

Trace Analysis: Dominance

Given

- a universal HyperLTL formula φ ,
- two traces t and t' , and
- a monitor automaton M_φ ,

t' dominates t if and only if for all trace variables π ,
 $L(M_\varphi[t'/\pi]) \subseteq L(M_\varphi[t/\pi])$.

Traces that are dominated by other traces in the stored trace set
can safely be **removed** from the trace set.

Specification Analysis: Symmetry example

Observational determinism

$$\forall \pi. \forall \pi'. (O_\pi = O_{\pi'}) W (I_\pi \neq I_{\pi'})$$

is **symmetric**, we can therefore **omit symmetric monitor instantiations**.

To prove the symmetry of observational determinism, we establish the validity of

$$\begin{aligned} \forall \pi. \forall \pi'. (O_\pi = O_{\pi'}) W (I_\pi \neq I_{\pi'}) \\ \leftrightarrow (O_{\pi'} = O_\pi) W (I_{\pi'} \neq I_\pi) \end{aligned}$$

Specification Analysis: Transitivity example

Output equality

$$\forall \pi. \forall \pi'. O_{\pi} = O_{\pi'}$$

is symmetric and **transitive**,
we therefore only need to store **a single trace**.

To prove the transitivity of output equality,
we establish the validity of

$$\begin{aligned} \forall \pi. \forall \pi'. \forall \pi''. (O_{\pi} = O_{\pi'}) \wedge (O_{\pi'} = O_{\pi''}) \\ \rightarrow (O_{\pi} = O_{\pi''}) \end{aligned}$$

Specification Analysis: Reflexivity example

Observational determinism

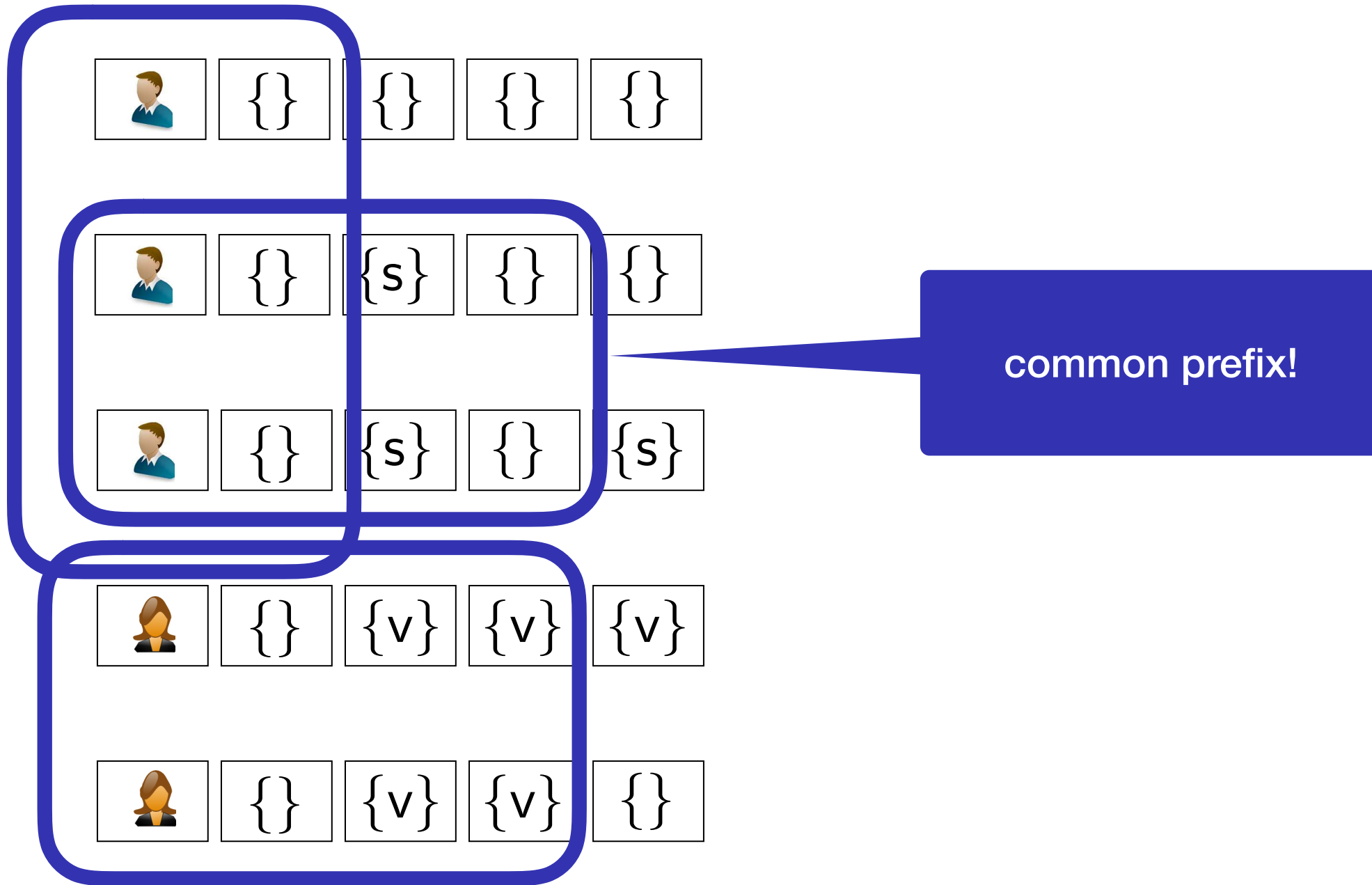
$$\forall \pi. \forall \pi'. (O_\pi = O_{\pi'}) \ W (I_\pi \neq I_{\pi'})$$

is **reflexive**, we can therefore **omit the reflexive monitor instantiations.**

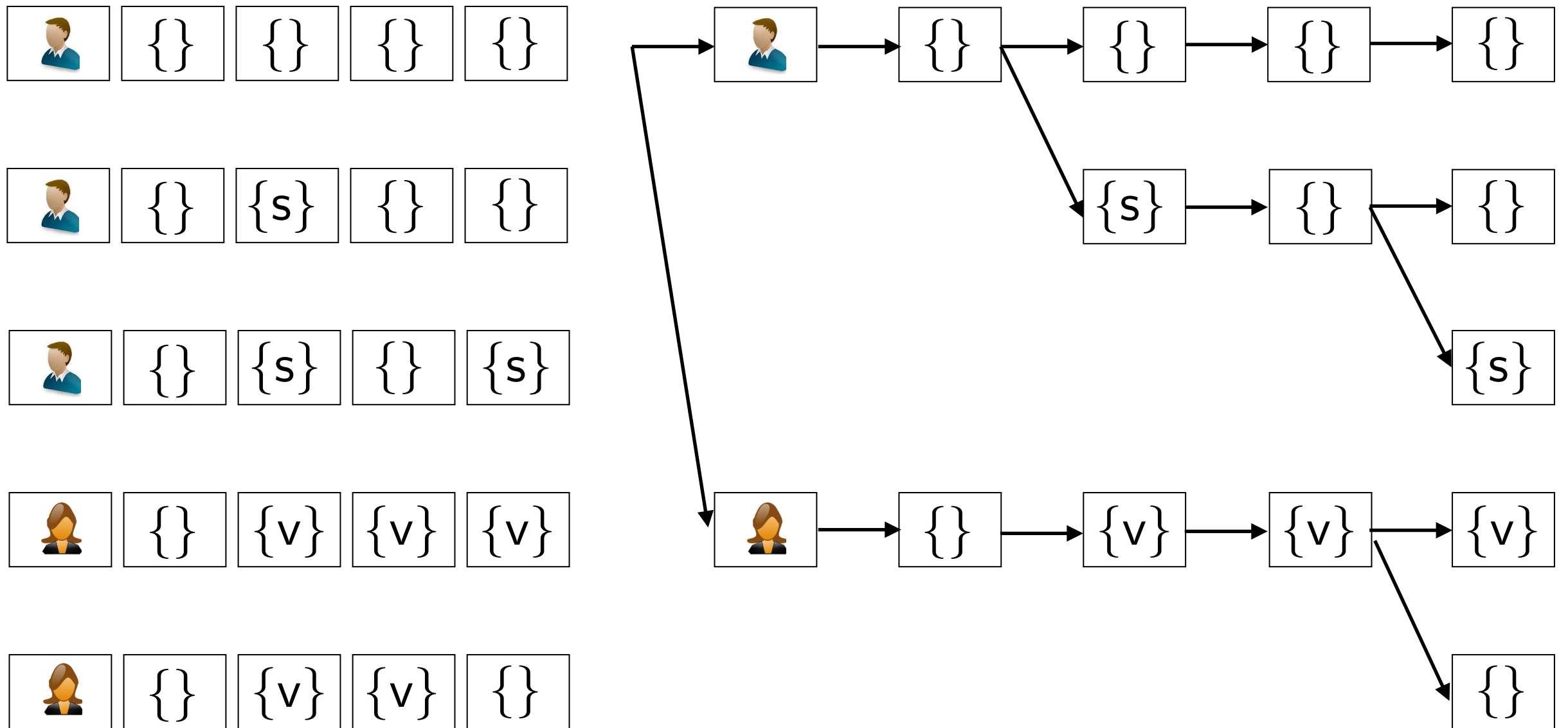
To prove the reflexivity of observational determinism, we establish the validity of

$$\forall \pi. (O_\pi = O_\pi) \ W (I_\pi \neq I_\pi)$$

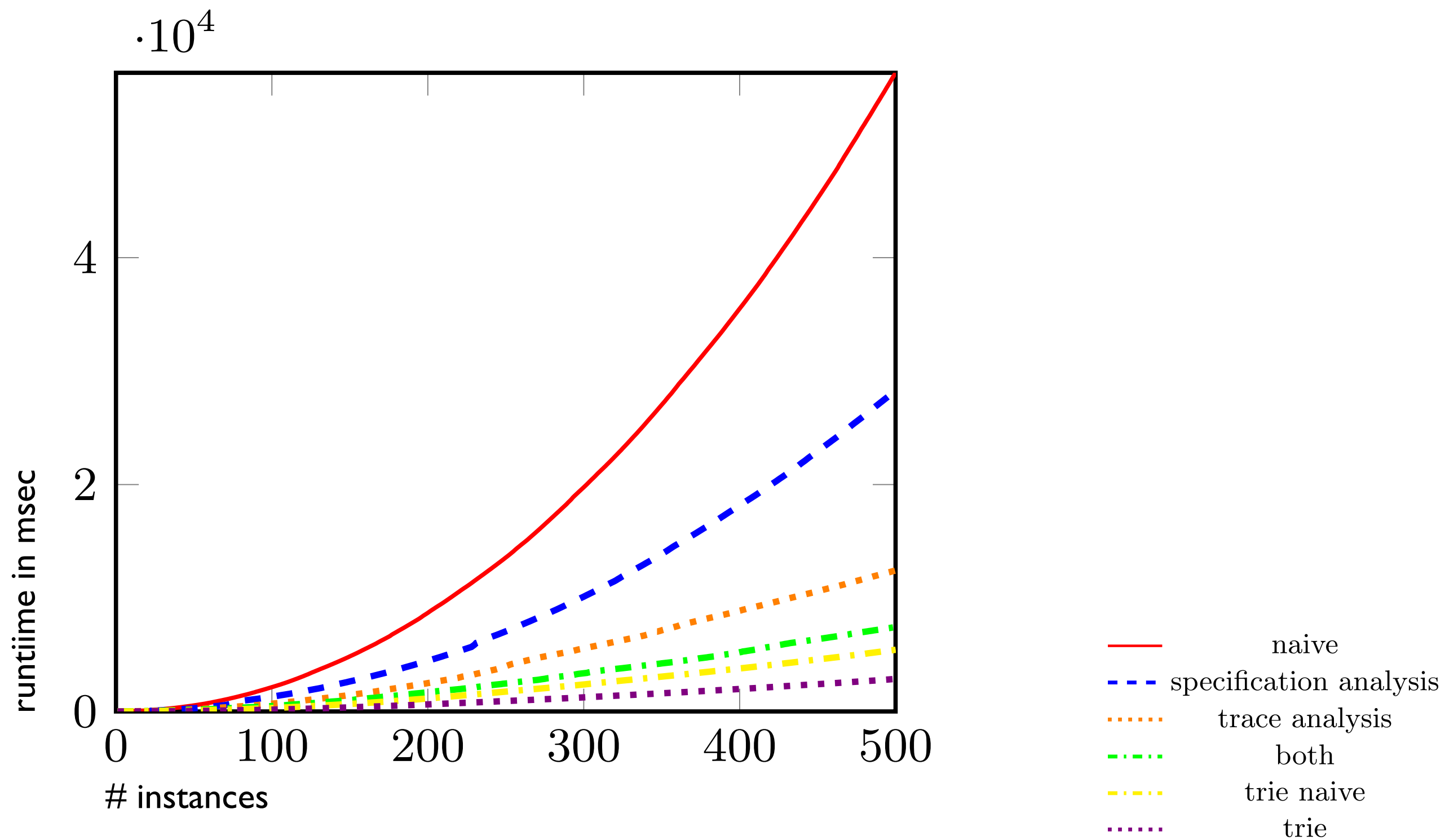
Trace Set Representation: Common Prefixes



Trace Set Representation: Prefix trees

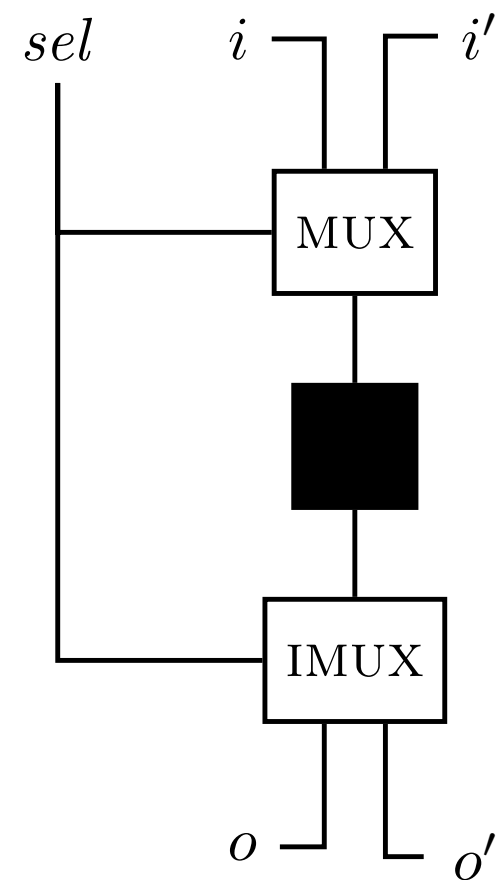
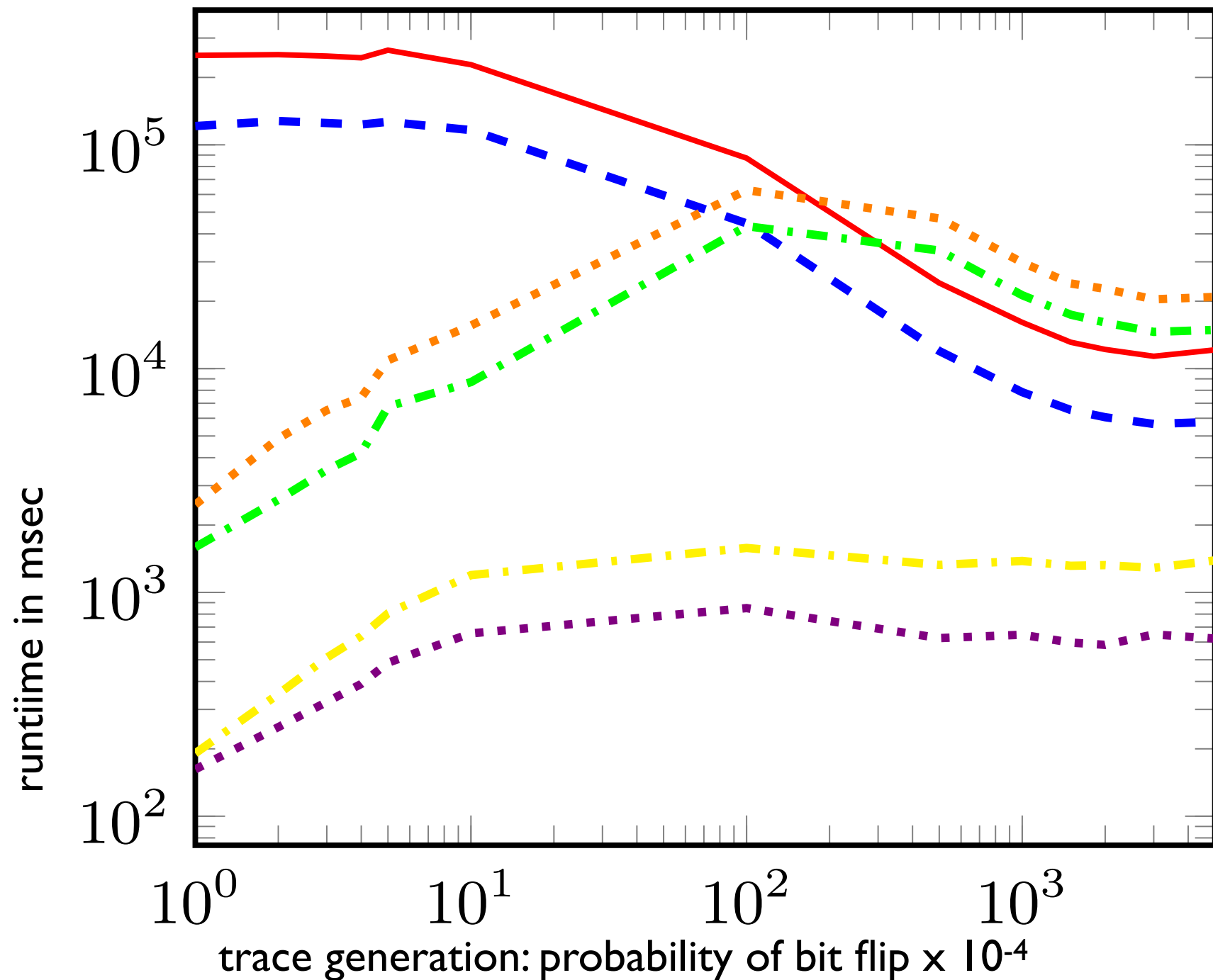


Experiments (error-resilient encoder)



Experiments (black-box circuit)

$$\forall \pi_1 \forall \pi_2. (\mathbf{o}_{\pi_1} = \mathbf{o}_{\pi_2}) \mathcal{W} (\bar{\mathbf{i}}_{\pi_1} \neq \bar{\mathbf{i}}_{\pi_2})$$



- naive
- - - specification analysis
- ... trace analysis
- · - both
- · · - trie naive
- · · trie

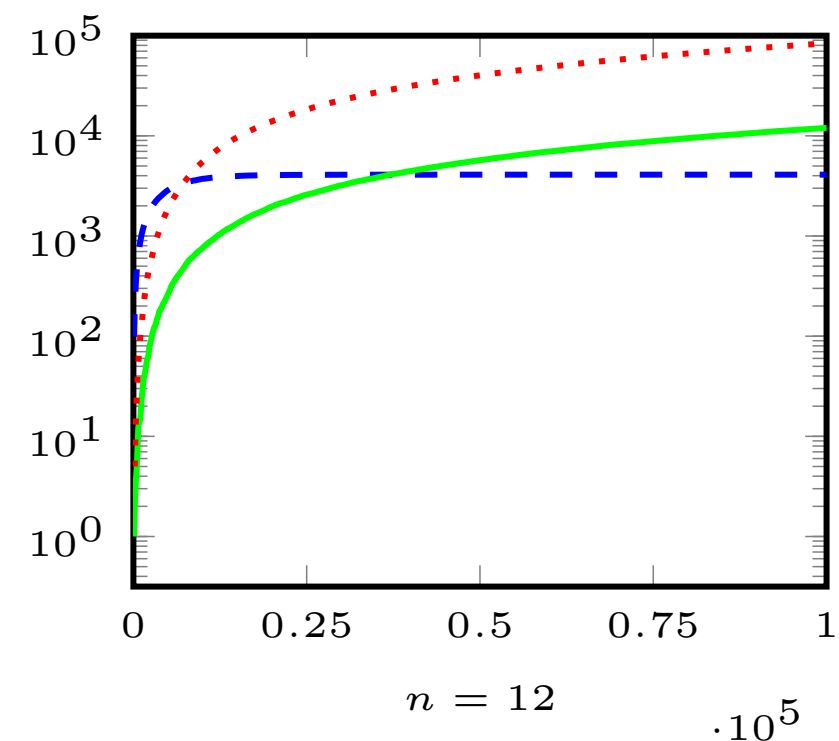
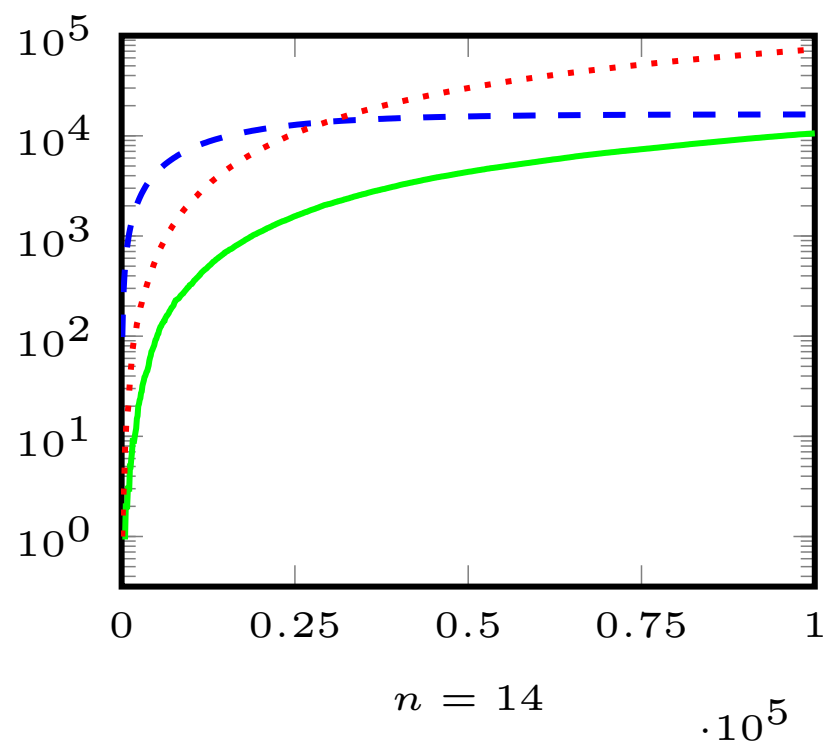
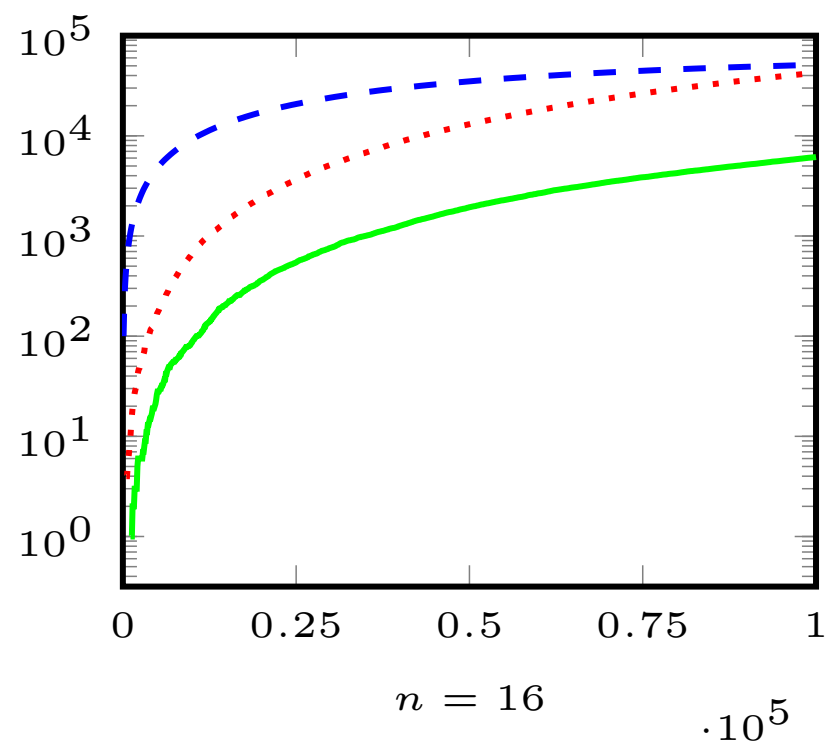
Experiments: Specification Analysis

		symm	trans	refl
ObsDet1	$\forall \pi. \forall \pi'. G (I_\pi = I_{\pi'}) \rightarrow G (O_\pi = O_{\pi'})$	✓	✗	✓
ObsDet2	$\forall \pi. \forall \pi'. (I_\pi = I_{\pi'}) \rightarrow G (O_\pi = O_{\pi'})$	✓	✗	✓
ObsDet3	$\forall \pi. \forall \pi'. (O_\pi = O'_{\pi'}) \mathcal{W} (I_\pi \neq I'_{\pi'})$	✓	✗	✓
QuantNoninf	$\forall \pi_0 \dots \forall \pi_c. \neg ((\bigwedge_i I_{\pi_i} = I_{\pi_0}) \wedge \bigwedge_{i \neq j} O_{\pi_i} \neq O_{\pi_j})$	✓	✗	✓
EQ	$\forall \pi. \forall \pi'. G (a_\pi \leftrightarrow a_{\pi'})$	✓	✓	✓
ConfMan	$\forall \pi \forall \pi'. ((\neg pc_\pi \wedge pc_{\pi'}) \rightarrow XG (s_\pi \rightarrow \bigcirc v_{\pi'}))$ $\wedge ((pc_\pi \wedge pc_{\pi'}) \rightarrow XG (v_\pi \leftrightarrow v_{\pi'}))$	✗	✗	✗

satisfiability checks < 2sec

Experiments: Trace Analysis

$$\forall \pi. \forall \pi'. G_{<n}(I_\pi = I_{\pi'}) \rightarrow G_{<n+c}(O_\pi = O_{\pi'})$$



- number of violations
- number of stored traces
- number of pruned traces

Conclusions

- Sequential monitoring of hyperproperties more difficult than parallel monitoring: in principle **unbounded memory**
- Effective optimizations:
 - Trace dominance: substantially reduces number of **stored traces**
 - Symmetry: omits at least **half** of the monitor instantiations
 - Transitivity: reduces the instantiations **to two**
 - Reflexivity: omits the **reflexive** monitor instantiations
 - Prefix tree representation: dramatically reduces memory & runtime
- Tools:
 - RVHyper: runtime verification tool for hyperproperties
 - EAHyper: satisfiability solver for hyperproperties

<https://www.react.uni-saarland.de/tools/>



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